		STUDY MODULE D	ESCRIPTION FORM		
	of the module/subject ching processes	Code 1010311261010315997			
Field of study			Profile of study	Year /Semester	
Elec	trical Engineerin	g	(general academic, practical) (brak)	3/6	
Elective path/specialty Distribution Devices and Electrical			Subject offered in: polish	Course (compulsory, elective) obligatory	
Cycle c	of study:		Form of study (full-time,part-time)		
First-cycle studies			full-time		
No. of I	nours			No. of credits	
Lectu	re: 2 Classes	s: 1 Laboratory: -	Project/seminars:	1 5	
Status	-	program (Basic, major, other)	(university-wide, from another	· · · · ·	
		(brak)		(brak)	
Educat	ion areas and fields of sci	ence and art		ECTS distribution (number and %)	
tech	nical sciences			5 100%	
Resp	onsible for subj	ect / lecturer:			
em tel. Wy	nž. Ryszrd Batura ail: ryszard.batura@pu 061 665 2767 dział Elektryczny Piotrowo 3A, 60-965 P				
		is of knowledge, skills and	d social competencies:		
		Fundamentals of the electrical d	evices and measuring equipme	ent and ots application.	
1	Knowledge		edge. Knowledge of the single- and three-phase AC systems and the electric power		
2	Skills				
		1c. Has understanding of the as decisions. Is able to work in the	team.		
3	Social competencies	Has basic knowledge of the cons power switches, MV switchgear networks and to carry out the ele	s, bus bars and bus ducts. Is a		
		ectives of the course:			
		g phenomena under the normal ar limiters? design method and pract		ition of skills to analyze it; getting	
	Study outco	mes and reference to the	educational results for	a field of study	
	vledge:				
rules,		ally underpinned knowledge of the of the electric circuits elements, ha ++1			
2. Has	knowledge of the phe	nomena accompanying the switch nalyze the transient states [K_		truct schemes of the electric	
		ected recovery voltage, over-volta erent DC and AC circuits - [K_W0		ff operation with and without the	
4. Is fa		calculation and limiting the latter	and can find it in analytical wa	y - [K_W04 ++]	
	carry out analysis of t	he simple electric system and dev	ices? operation implementing	adequate methods and tools	
	use data sheets and ap	oplication notes to choose the pro	per elements of the electric ne	twork or system being designed.	
Soci	al competencies:				
gradua		need and knows opportunities of the need for upgrading the profes			

Assessment methods of study outcomes

Lectures:

?Assessment of the knowledge and skills during the problem-type written examination,

?Continuous assessment, at each class (bonus for activity and perception quality).

Classes

?Test and bonus for a knowledge necessary to solve tasks in the scope of the lectures? subjects.

?Assessment of the skills related to the class task accomplishment.

Projects:

?Test and bonus for a knowledge necessary to accomplish the design task,

?Assessment of the knowledge and skills related to the design task accomplishment.

Adding extra points for activity in discussions, especially for:

?effectiveness of implementation of the knowledge acquired when solving a given problem.

?ability to cooperate in the team accomplishing in practice a specific task in lab.

?remarks related to the educational materials? enhancement,

?care and esthetic form of the elaborated designs ? within the individual work.

Course description

DC cut off(breaking (the cut off conditions referring to the voltage-current curves and power balance for the constant and variable arc length, electric arc?s limit length, finding the arc duration and cut off operation in the analytical and graphical way) and description of influence of the circuit parameters and arc ignition manner on the switching overvoltages. Conditions of the AC current cut-off nearby the natural current-zero crossing and using step-up voltage. Post-arc end electric resistance of the contact gap for the short and long arcs. Development of the electric power network elements? scheme for the transient states analysis (equivalent schemes of generators, transformers, overhead and cable lines. Current and voltage transformers, bus bar). Finding overvoltages, overcurrents and recovery voltages during the fault clearance in the three-phase circuits with isolated and earthened neutral point; resistive load currents? switching; switching on and cut off of condensers with and without electric arc re-ignition; cut off of the low inductive currents and asynchronous motors. Overvoltage and overcurrent limiting methods. Discussions and design work with the lecture-related subjects.

Basic bibliography:

1. Królikowski Cz.: Inżynieria łączenia obwodów elektrycznych wielkich mocy, Wydawnictwo Politechniki Poznańskiej, 1998.

2. Królikowski Cz.: Technika łączenia obwodów elektroenergetycznych, WNT, Warszawa, 1990.

3. Bolkowski St.: Teoria obwodów elektrycznych, WNT, Warszawa, 1995.

Additional bibliography:

1. Magazins Elektroinstalator, Elektroinfo.

- 2. Related standards.
- 3. Manufacturers? data sheets.
- 4. Internet publications

Result of average student's workload

Activity		Time (working hours)
1. Lectures	30	
2. Classes	15	
3. Projects	15	
4. Part in consultations	35	
5. The preparation to occupations, the study of laboratory documentation		30
Student's wo	orkload	
Source of workload	hours	ECTS
Total workload	125	5
Contact hours	90	3
Practical activities	60	2